

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10702437			
<b>Filing Date:</b>	07-Nov-2003			
<b>Title of Invention:</b>	Substrate-biased silicon diode for electrostatic discharge protection and fabrication method			
<b>First Named Inventor/Applicant Name:</b>	Chyh-Yih Chang			
<b>Filer:</b>	Clark A. Jablon/Megumi McKeever			
<b>Attorney Docket Number:</b>	06720.0064-02			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 3 months with \$120 paid	1253	1	900	900

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				900